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	ISSUE CLASSIFICATION
Subclass	
Class	

PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
SCANNED ✓Km1 o.a. 1c	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/912745		713 710	306	2181	Lefkowitz Auve

APPLICANTS

Richard Zaborski

TITLE

Bus bridge circuit including audio logic and an addressable register for storing an address bit used when the audio logic accesses digital data, and method for initializing a chip set including the bus bridge circuit

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)						
CLASS	**SUBCLASS**	**CLASS**	**SUBCLASS (ONE SUBCLASS PER BLOCK)**						
INTERNATIONAL CLASSIFICATION									
						☐ Continued on Issue Slip Inside File Jacket			

☐ **TERMINAL DISCLAIMER**	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner)		(Date)	**NOTICE OF ALLOWANCE MAILED**	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner)		(Date)	**ISSUE FEE**	
				Amount Due	Date Paid
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